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FORMATION DISCLOSURE STATEMENT BY APPLICANT

	ATTY. DOCKET NO. MIC-41	APPLICATION NO. 10/719,348			
	APPLICANT Seong-Hoon Lee	CONFIRMATION NO. Not yet assigned			
	FILING DATE November 21, 2003	GROUP 28/6			

U.S. PATENT DOCUMENTS

EXAMINER WITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
Km	5,463,337	10/31/95	Leonowich	327	158 ,	
·	6,194,947	02/27/01	Lee et al.	327	359	
	6,295,328	09/25/01	Kim et al.	375	376	
	6,313,688	11/06/01	Lee et al.	327	359	
	6,326,826	12/04/01	Lee et al.	327	161	
	6,366,148	04/02/02	Kim	327	262	
	6,512,408	01/28/03	Lee et al.	327	359	
	6,573,771	06/03/03	Lee et al.	327	158	
	6,642,760	11/04/03	Alon et al.	327	158	
	6,661,863	12/09/03	Toosky	375	376	
	2003/0219088 A1	12/30/02	Kwak	375	376	

EXAMINER INITIAL

Jong-Tae Kwak, A Low Cost High Performance Register-Controlled Digital DLL for 1 Gbps x32 DDR SDRAM, The 8th Korean Conference on Semiconductors, February 2001.

Ramin Farjad-Rad, A Low-Power Multiplying DLL for Low-Jitter Multigigahertz Clock Generation in Highly Integrated Digital Chips, IEEE Journal of Solid-State Circuits, Vol. 37,. No. 12, December 2002, p. 1804-1812.

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

All references have been considered.

EXAMINER INITIAL

EXAMINER

WELLS

DATE CONSIDERED

1/3/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not conformance and not considered. Include copy of this form with next communication to applicant.